



INTERNATIONAL CONFERENCE

ON

SEMICONDUCTOR CHIP DESIGN AND MANUFACTURING

IU-ICSCDM-2025

Friday 20 to Sunday 22 June, 2025
Venue: Indrashil University



इलेक्ट्रॉनिक्स एवं
सूचना प्रौद्योगिकी मंत्रालय
**MINISTRY OF
ELECTRONICS AND
INFORMATION TECHNOLOGY**

सत्यमेव जयते



ABOUT INTERNATIONAL CONFERENCE:

The International Conference on Chip Design and Semiconductor Manufacturing, organized by Indrashil University in alignment with the India Semiconductor Mission and Gujarat State Electronics Mission, aims to bring together global experts, industry leaders, and researchers to explore the latest advancements and innovations in the semiconductor and chip design sectors. The conference will focus on fostering collaboration, knowledge sharing, and research in key areas such as semiconductor fabrication processes, chip architecture, electronic packaging, and emerging technologies in the semiconductor field. Possible tracks for the conference include Advanced Semiconductor Materials, VLSI Design and Optimization, Fabrication and Manufacturing Techniques, Internet of Things (IoT) and Semiconductor Integration, AI and Machine Learning in Chip Design, and Sustainability in Semiconductor Manufacturing. Themes will also address the future of semiconductor supply chains, innovations in quantum computing, and the role of semiconductors in powering next-generation technologies.

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indrashiluniversity.edu.in

ABOUT INDRASHIL UNIVERSITY :

Indrashil University, located in Gujarat, India, is a premier educational institution focused on fostering innovation, research, and excellence in various fields of technology, engineering, pharmacy and applied sciences. Established with the vision to provide world-class education and contribute to the nation's technological and industrial growth, the university emphasizes practical learning, interdisciplinary research, and industry collaboration. With state-of-the-art infrastructure, cutting-edge laboratories, and a strong focus on industry-oriented programs, research oriented approach, Indrashil University offers a vibrant academic environment that nurtures creativity, entrepreneurship, and leadership. The university is committed to producing skilled professionals who are well-equipped to meet the dynamic challenges of global industries, especially in fields like semiconductor manufacturing, chip design, and advanced technologies. It has the state of art facility like NMR Machine for carrying out testing and the state of art research. The School of Engineering offers B.Tech program in Electronics (VLSI Design and Technology) along with B.Tech in Computer Science and Engineering, B.Tech in Information Technology and B.Tech in Chemical and Bio Chemical Engineering. The University has signed MoU with CSIR Labs, SAC-ISRO, and eInfochips, Electromech Cloud Tech, Laurentian University, Canada and Sacred Heart University, USA.

SCOPE OF THE INTERNATIONAL CONFERENCE:

Areas: All areas of Chip Design and Semiconductors Manufacturing from advance semiconductor material to devices and systems for space sector, biomedical sector, automobile sector, computing sector, analog, digital and mixed mode design, ASIC Design, RFIC Design, AI and ML in Chip Design. The research and development work in the following areas are solicited but not limited to

- Advances in Semiconducting Materials
- Low-Power Chip Design Techniques
- Signal Integrity and Timing Analysis
- Design for Testability (DFT)
- Yield Enhancement in Chip Manufacturing
- Wafer Fabrication and Process Flow
- Defect Detection and Failure Analysis
- Materials Science for Semiconductor
- Next-Generation Semiconductor Materials: SiC, GaN, etc.

- Advanced EDA Tools and AI-Driven Design Automation
- Embedded Systems and Applications
- Controller Design
- Testing and Verification

CONFERENCE INCLUSION:

- Keynote Speech : 01
- Plenary Talk : 02
- Invited and Contributed Oral Research Presentation
- Poster Presentation on Design and Implementation
- Special Industry Track
- Pre Conference Workshops and Tutorials
- Exhibition
- Student Projects in the Field of Electronics, Electronics and Communication
- Panel Discussion

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REGISTRATION can be done by filling form on www.indrashiluniversity.edu.in or by scanning QR Code.

REGISTRATION FEE:

Students	: INR 1500/-
Ph.D Scholar	: INR 2000/-
Academia	: INR 5000/-
R&D Organizations	: INR 10,000/-
Industry	: INR 10,000/-

SCOPE OF SPONSORSHIPS:

1. Platinum	- INR 75,000/-
2. Gold	- INR 50,000/-
3. Silver	- INR 25,000/-
Exhibition	: INR 25,000/-

IMPORTANT DATES

Registration starts	: April 1, 2025
Abstract Submission	: April 30, 2025
Abstract Acceptance	: May 7, 2025
Full Paper Submission	: May 15, 2025
Paper Acceptance	: May 20, 2025
Final Paper Submission	: June 1, 2025
Registration Closes	: June 15, 2025

REGISTRATION INCLUDES : Registration Material, Attendance to all sessions, Lunch and Snacks.

For more details visit : www.indrashiluniversity.edu.in

Paper Submission : Standard 2 Column Format with Page limit of 6 pages

Abstract and Paper can be submitted by sending an email at scdm2025@indrashiluniversity.edu.in as a single PDF form with author affiliation details

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